

70

4855956

SERIAL NUMBER (Series of 1987) <b>103312</b>	PATENT DATE <b>AUG 08 1989</b>		PATENT NUMBER <b>4855956</b>	
SERIAL NUMBER <b>07/103,312</b>	FILING DATE <b>10/01/87</b>	CLASS <b>365</b>	SUBCLASS <b>210</b>	GROUP ART UNIT <b>233</b>
EXAMINER <b>12 10/01/89</b>				

APPLICANTS  
TAKAHIKO HIRAI, TOKYO, JAPAN.

\*\*CONTINUING DATA\*\*\*\*\*  
VERIFIED

*None/R*

\*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\*  
VERIFIED JAPAN 61-234929 10/01/86

*Checkoff*

Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes	<input type="checkbox"/> no	AS FILED	STATE OR COUNTRY	SHEETS DRWGS.	TOTAL CLAIMS	INDEP. CLAIMS	FILING FEE RECEIVED	ATTORNEY'S DOCKET NO.
Verified and Acknowledged	<i>SP</i>					7	2	450.00	0-15952

ADDRESS

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TITLE

SEMICONDUCTOR MEMORY DEVICE WITH IMPROVED CELL ARRANGEMENT

U.S. DEPT. OF COMM-PAT. & TRADEMARK OFFICE (rev. 10-78)

**CERTIFICATE**

FEB 12 1989

**OF CORRECTION**

PARTS OF APPLICATION  
FILED SEPARATELY

*3 - sub. pts. of Dep. Ser. 3, 1989*

NOTICE OF ALLOWANCE MAILED

**MAR 24 1989**

ISSUE FEE

Amount Due  
*620.00*

Date Paid  
*6/12/89*

PREPARED FOR ISSUE 3-27-89

CLAIMS ALLOWED

Total Claims <i>6</i>	Print Claim <i>1</i>
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DRAWING

Sheets Drwg. <i>3</i>	Figs. Drwg. <i>6</i>	Print Fig. <i>3</i>
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JOSEPH A. POPEK  
PRIMARY EXAMINER  
ART UNIT 233

Primary Examiner

ISSUE CLASSIFICATION

Class <i>365</i>	Subclass <i>210</i>
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ISSUE  
BATCH  
NUMBER

*R38*

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PATENT NUMBER		ORIGINAL CLASSIFICATION	
1033310		CLASS	SUBCLASS
		365	210
APPLICATION SERIAL NUMBER		CROSS REFERENCE(S)	
1033310		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
		365	51
APPLICANT'S NAME (PLEASE PRINT)		IF REEQ-US, ORIGINAL PATENT NUMBER	
Lifac		G11C 7/02	
INTERNATIONAL CLASSIFICATION (INT. CL. 4)		GROUP	ASSISTANT EXAMINER (PLEASE STAMP OR PRINT FULL NAME)
G11C 7/02		233	PRIMARY EXAMINER (PLEASE STAMP OR PRINT FULL NAME)
			JSCPL A. COPE
PTO-270 (10-84)			
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			
ISSUE CLASSIFICATION SLIP			